

Form PTO-1449 (Modified)
(Rev. 11/92)U.S. Dept. of Commerce
Patent and Trademark Office**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use several sheets if necessary)

(37 CFR 1.98(b))

ATTY. DOCKET NO.

SERIAL NO.

0254-094-PCT-2-USCIP-1

10/814,532

APPLICANT

Cady, et al

FILING DATE

3/31/04

GROUP

2815

U.S. PATENT DOCUMENTS

*Examiner Initial	Patent Number		Patentee	Class	Subclass	Filing Date (if appropriate)
gbe	6,222,737B1	4/24/2001	Ross, Andrew C.	361/767		4/23/1999
	6,360,433B1	3/26/2002	Ross, Andrew C.	29/840		9/19/200
	5,428,190	6/27/1995	Stopperan	174/261		7/2/1993
	6,097,087	8/1/2000	Farnworth, Warren M. et al	257/698		10/31/1997
	6,323,060B1	11/27/2001	Isaak	438/109		5/5/1999
	6,351,029B1	2/26/2002	Isaak	257/688		5/19/2000
	6,368,896B2	4/9/2002	Farnworth, Warren M. et al	438/113		4/23/1999
	6,410,857B1	6/25/2002	Gonya	174/254		8/13/2001
	6,426,240B2	7/30/2002	Isaak	438/106		6/25/2001
	6,426,549B1	7/30/2002	Isaak, Harlan R.	257/686		11/3/2000
	6,444,921B1	9/3/2002	Wang, et al	174/260		2/3/2000
	6,465,877B1	10/15/2002	Farnworth, Warren M. et al	257/668		3/27/2000
gbe	6,514,793B2	2/4/2003	Isaak	438/109		2/4/2003

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
gbe	58-96756(A)	06/08/1983	Japan			Partial	
	58-112348(A)	07/04/1983	Japan			Partial	
	60-254762	12/1985	Japan			Partial	
gbe	EP 0 298 211 A#	01/11/1989	European			X	

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gbe	Dense-Pac Microsystems, Breaking Space Barriers, 3-D Technology 1993
	Dense-Pac Microsystems, DPS512X16A3, Ceramic 512K X 16 CMOS SRAM Module
	"Design Techniques for Ball Grid Arrays," William R. Newberry, Xynetix Design Systems, Inc.

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nbe	6,262,895 B1	7/17/2001	Forthum, John A.	361/749		1/13/2000
	5,239,198	8/24/1993	Lin et al.	257/693		7/2/1992
	6,028,365	2/22/2000	Akram et al.	257/778		3/30/1998
	6,433,418	8/13/2002	Fujisawa et al.	257/696		4/29/1999
	6,208,521 B1	3/27/2001	Nakatsuka, Yasuo	361/749		5/19/1998
	6,265,660 B1	7/24/2001	Tandy, Patrick W.	174/52.4		8/18/2000
	6,300,679 B1	10/9/2001	Mukerji et al.	257/738		6/1/1998
	6,462,412 B2	10/8/2002	Kamei et al	257/723		1/17/2001
	6,465,893 B1	10/15/2002	Khandros et al.	257/777		10/19/2000
	6,486,544 B1	11/26/2002	Hashimoto, Nobuaki	257/686		9/3/1999
	6,560,117 B2	5/6/2003	Moon, Ow Chee	361/749		8/30/2001
	6,590,282 B1	7/8/2003	Wang et al.	257/686		4/12/2002
nbe	6,600,222 B1	7/29/2003	Levardo, Melvin N.	257/686		7/17/2002

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						Yes	No
nbe	62-230027(A)	10/08/1987	Japan			Partial	

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(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

nbe	"Chip Scale Packaging and Redistribution," Paul A. Magill, Glenn A. Rinne, J. Daniel Mis, Wayne C. Machon, Joseph W. Baggs, Unitive Electronics Inc.
	"3D Interconnection for Ultra-Dense Multichip Modules," Christian VAL, Thomson-CSF DCS Computer Division, Thierry Lemoine, Thomson-CSF RCM Radar Countermeasures Division
	"High Density Memory Packaging Technology High Speed Imaging Applications," Dean Frew, Texas Instruments Incorporated
nbe	Teresa Technologies, Inc. - Semiconductor Intellectual Property, Chip Scale Packaging - Website pages (3)

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gbe	6,473,308B2	10/29/2002	Forthun	361/749		04/19/2001
	US2002/0006032A1	01/17/2002	Karabatsos	361/760		01/11/2001
	US2002/0101261A1	08/01/2002	Karabatsos	326/83		01/29/2002
	3,654,394	04/04/1972	Gordon	179/15 BL		07/08/1969
	4,513,368	04/23/1985	Houseman	364/200		05/22/1981
	4,645,944	02/24/1987	Uya	307/243		09/04/1984
	4,733,461	03/29/1988	Nakano	29/830		12/24/1985
	4,862,249	08/29/1989	Carlson	357/80		04/17/1987
	4,891,789	01/02/1990	Quattrini, et al	365/63		03/03/1988
	5,261,068	11/09/1993	Gaskins, et al.	395/425		05/25/1990
	5,543,664	08/06/1996	Burns	257/787		01/20/1995
	5,566,051	10/15/1996	Burns	361/704		08/30/1994
	5,953,215	09/14/1999	Karabatsos	361/767		06/26/1998
	6,266,252B1	07/24/2001	Karabatsos	361/788		09/13/1999
	6,233,650B1	05/15/2001	Johnson et al.	711/5		04/01/1998
gbe	6,446,158B1	09/03/2002	Karabatsos	711/5		05/17/2000

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Document Number	Date	Country	Class	Subclass	Translation	
					Yes	No

OTHER DOCUMENTS

(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

gbe	"Tessera Introduces uZ™ - Ball Stacked Memory Package for Computing and Portable Electronic Products" Joyce Smaragdis, Tessera Public Relations; 2 figures that purport to be directed to the uZ™ - Ball Stacked Memory Package.
	Ron Bauer, Intel. "Stacked-CSP Delivers Flexibility, Reliability, and Space-Saving Capabilities", Spring 2002.
	Tessera uZ Ball Stack Package. 4 figures that purport to be directed to the uZ - Ball Stacked Memory, 1 Page.
gbe	"Vertically-Integrated Package." Alvin Weinberg, Pacesetter, Inc. and W. Kinzy Jones, Florida International University

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U.S. PATENT DOCUMENTS

*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
7/22	6,620,651B2	9/16/2003	He et al.	438/113		10/23/2001
	US2002/0139577A1	10/3/2002	Miller, Charles A.	174/261		3/27/2001
	US2002/0164838A1	Nov-02	Moon et al.	438/107		6/5/2001
	US2002/0180022A1	12/5/2002	Emoto, Yoshiaki	257/686		7/19/2002
	6,072,233	6/6/2000	Corisis et al.	257/686		5/4/1998
	3,436,604	04/01/1969	Hyltin	317/101		04/25/1966
	3,746,934	07/17/1973	Stein	317/101		05/06/1971
	3,772,776	11/20/1973	Weisenburger	29/628		12/03/1969
	4,288,841	09/08/1981	Gogal	361/414		09/20/1979
	4,696,525	09/29/1987	Coller, et al	439/69		12/13/1985
	4,823,234	04/18/1989	Konishi, et al	361/386		07/01/1986
	4,833,568	05/23/1989	Berhold	361/383		01/29/1988
	4,911,643	03/27/1990	Perry, et al	439/67		08/03/1989
	4,953,060	08/28/1990	Lauffer, et al	361/388		05/05/1989
	4,956,694	09/11/1990	Eide	357/74		11/04/1988
7/22	4,983,533	01/08/1991	Go	437/7		10/28/1987

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Document Number	Date	Country	Class	Subclass	Translation	
					Yes	No

OTHER DOCUMENTS

(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

7/22	Dense-Pac Microsystems, 256 Megabyte CMOS DRAM DP3ED32MS72RW5
7/22	IBM Preliminary 168 Pin SDRAM Registered DIMM Functional Description & Timing Diagrams

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7/2	5,041,015	08/20/1991	Travis	439/492		03/30/1990
	5,099,393	03/24/1992	Bentlage, et al	361/413		03/25/1991
	5,138,430	08/11/1992	Gow, 3 rd , et al	357/70		06/06/1991
	5,159,434	10/27/1992	Kohn, et al	357/80		02/01/1991
	5,222,014	06/22/1993	Lin	361/414		03/02/1992
	5,224,023	06/29/1993	Smith, et al.	361/412		02/10/1992
	5,241,454	08/31/1993	Amecn, et al	361/744		01/22/1992
	5,247,423	09/21/1993	Lin, et al	361/719		05/26/1992
	5,259,770	11/09/1993	Bates, et al	439/66		03/19/1992
	5,281,852	01/25/1994	Normington	257/685		12/10/1991
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	5,375,041	12/20/1994	McMahon	361/749		12/02/1992
	5,484,959	01/16/1996	Burns	174/524		12/11/1992
	5,572,065	11/05/1996	Burns	257/666		10/24/1994
	5,612,570	03/18/1997	Eide, et al	257/686		04/13/1995
	5,644,161	07/01/1997	Burns	257/668		06/07/1995
	5,776,797	07/07/1998	Nicewarner, Jr., et al	438/107		07/02/1997
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U.S. PATENT DOCUMENTS

*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
<i>me</i>	5,778,552	7/14/1998	LeGuin	33/559		5/8/1996
	5,805,422	09/08/1998	Otake, et al	361/749		08/15/1997
	5,869,353	02/09/1999	Levy, et al	438/109		11/17/1997
	5,926,369	07/20/1999	Ingraham, et al	361/699		01/22/1998
	5,949,657	09/07/1999	Karabatsos	361/803		08/25/1998
	6,014,316	01/11/2000	Eide	361/735		6/10/1998
	6,172,874B1	01/09/2001	Bartilson	361/719		04/06/1998
	6,208,521B1	3/27/2001	Nakatsuka	361/749		5/19/1998
	6,225,688B1	05/01/2001	Kim et al.	257/686		02/04/1999
	6,281,577B1	08/28/2001	Oppermann et al.	257/724		04/22/1997
<i>me</i>	6,392,162B1	05/21/2002	Karabatsos	174/261		11/10/2000

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James W. Cady, et al.

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GROUP

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*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
nlc	US 5,117,282	05/26/1992	Salatino	357	81	10/29/1990
	US 5,448,511	09/05/1995	Paurus, et al.	365	52	06/01/1994
	US 5,642,055	06/24/1997	Difrancesco	324	757	05/12/1995
	US 5,917,709	06/29/1999	Johnson, et al.	361	803	06/12/1997
	US 6,121,676	09/19/2000	Solberg	257	686	12/11/1997
	US 6,205,654 B1	03/27/2001	Burns	29	830	12/28/1998
	US 6,360,433 B1	03/26/2003	Ross	29	840	09/19/2000
nlc	US 6,376,769 B1	04/23/2002	Chung	174	522	03/14/2000

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

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nlc	2003309246(A)	10/31/2003	Japan			Partial	
	2003086761(A)	3/20/2003	Japan			Partial	
	2003086760(A)	3/20/2003	Japan			Partial	
	2003037246(A)	2/7/2003	Japan			Partial	
	2001332683(A)	11/30/2001	Japan			Partial	
	2001085592(A)	3/30/2001	Japan			Partial	
nlc	2001077294(A)	3/23/2001	Japan			Partial	

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jer	US 6,552,910 B1	04/22/2003	Moon, et al.	361	749	06/28/2000
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	US 6,660,561 B2	12/09/2003	Forthun	438	109	07/24/2002
	US 6,677,670 B2	01/13/2004	Kondo	257	686	04/25/2001
	2004/0150107 A1	08/05/2004	Cha, et al.	257	738	12/29/2003
	2004/0021211 A1	02/05/2004	Damberg	257	282	09/06/2002
	2003/0168725 A1	09/11/2003	Warner et al.	257	686	10/28/2002
jac	2001/0040793 A1	10/15/2001	Inaba	361	749	01/31/2001

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